

M68HC11 and X2402 Family

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0234 *****
0235 * CLOCK IN A 1 TO THE DUT. THIS ROUTINE IS *
0236 * CALLED WHEN A READ SEQUENCE HAS FINISHED. *
0237 *****
0238
0239 E11A BD E1 24 NACK: JSR CLOCK GENERATE A CLOCK PULSE
0240 E11D 36 PSHA
0241 E11E 86 03 LDAA #SDAOUT MAKE SDA AN OUTPUT
0242 E120 A7 09 STAA DDRD,X
0243 E122 32 PULA
0244 E123 39 RTS
0245
0246 *****
0247 * ISSUE A CLOCK PULSE. WHILE THE CLOCK IS HIGH THE VALUE ON THE *
0248 * SDA LINE IS PLACED IN THE LSB OF THE A REG. WHEN A READ IS TAKING *
0249 * PLACE THE LSB OF THE A REG WILL INDICATE THE VALUE FROM THE DUT. *
0250 *****
0251
0252 E124 1C 08 02 CLOCK: BSET PORTD,X #SCLBIT PROVIDE A CLOCK ON SCL, START HIGH
0253 E127 A6 08 LDAA PORTD,X READ SDA WHILE SCL IS HIGH
0254 E129 1D 08 02 BCLR PORTD,X #SCLBIT
0255 E12C 84 01 ANDA #$01 SDA VALUE IS IN LOWER BIT OF A REG
0256 E12E 39 RTS
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